

18379 U.S. PTO
121103

PATENT APPLICATION
LSI Docket No.: 01-916/1D
Date: December 11, 2003

17302 U.S. PTO
10/733034
121103

Attn: Mail Stop Patent Application
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

DIVISIONAL APPLICATION REQUEST TRANSMITTAL

Transmitted herewith for filing is the divisional patent application of
Inventor(s): Kunal N. Taravade and Gregory A. Johnson

For (title): **Method Of Fabricating An Integral Capacitor And Gate Transistor Having Nitride And
Oxide Polish St Layers Using Chemical Mechanical Polishing Elimination**

Enclosed are:

- [x] Application Data Sheet
- [x] Divisional Patent Application (33 pages of text)
- [x] Copy of Declaration and Power of Attorneys from priority application
- [x] Copy of Assignments from priority application
- [x] Recordation Cover Sheet
- [x] Seven (7) sheets of Formal drawings (2 sets)
- [x] Preliminary Amendment
- [] It is expressly requested that the U.S. Patent and Trademark Office
commence national processing of the above-entitled international
application under the provisions of PCT Article 23(2) and 35 USC 371(f).
- [x] Nonpublication Request Under 35 U.S.C. 122(b) (2) (B) (i)
- [x] Return Postcard

Correspondence Address:

Timothy Croll
LSI Logic Corporation
Intellectual Property Department
M/S D-106
1551 McCarthy Boulevard
Milpitas, California 95035



Attorney or Agent of Record

Reg. No. Harold C. Moore
37,892

The filing fee has been calculated as shown below:

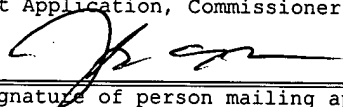
CLAIMS AS FILED

FOR	NUMBER FILED	NUMBER EXTRA	RATE	FEE
TOTAL CLAIMS	5 - 20 =	0	x \$18	0.00
INDEPENDENT CLAIMS	1 - 3 =	0	x \$84	0.00
			BASIC FEE	\$770.00
			FILING FEE	\$770.00

- The Commissioner is hereby authorized to charge the basic filing fee (\$770.00) to **Deposit Account No. 12-2252**. Two copies of this sheet are enclosed.
- The Commissioner is hereby authorized to charge any fees under 37 CFR §1.16 and 1.17 which may be required during the pendency of the application to **Deposit Account No.12-2252**.

"Express mail" mailing label number EL975501155US Date of Deposit December 11, 2003
I hereby certify that this correspondence is being deposited with the United States Postal Service "Express Mail Post Office to Addressee" service under 37 CFR §1.10 on the date indicated above and is addressed Mail Stop Patent Application, Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1459

Harold C. Moore
(Name of person mailing application)


(Signature of person mailing application)

Under the Paperwork Reduction Act of 1995, no persons are required to respond to a collection of information unless it displays a valid OMB control number.

**NONPUBLICATION REQUEST
UNDER
35 U.S.C. 122(b)(2)(B)(i)**

First Named Inventor

Kunal N. Taravade

~~Method of Fabricating An Integral
Capacitor and Gate Transistor
Having Nitride and Oxide Polish
St Layers Using Chemical Mechanical
Polishing Elimination~~

Attorney Docket Number

1003-0637

I hereby certify that the invention disclosed in the attached application **has not and will not be** the subject of an application filed in another country, or under a multilateral agreement, that requires publication at eighteen months after filing.

I hereby request that the attached application not be published under 35 U.S.C. 122(b).

12/11/03

Date



Signature

(317) 638-2922

Telephone number

Harold C. Moore

Typed or printed name

This request must be signed in compliance with 37 CFR 1.33(b) and submitted with the application **upon filing**.

Applicant may rescind this nonpublication request at any time. If applicant rescinds a request that an application not be published under 35 U.S.C. 122(b), the application will be scheduled for publication at eighteen months from the earliest claimed filing date for which a benefit is claimed.

If applicant subsequently files an application directed to the invention disclosed in the attached application in another country, or under a multilateral international agreement, that requires publication of applications eighteen months after filing, the applicant **must** notify the United States Patent and Trademark Office of such filing within forty-five (45) days after the date of the filing of such foreign or international application. **Failure to do so will result in abandonment of this application (35 U.S.C. 122(b)(2)(B)(iii)).**

This collection of information is required by 37 CFR 1.213(a). The information is required to obtain or retain a benefit by the public which is to file (and by the USPTO to process) an application. Confidentiality is governed by 35 U.S.C. 122 and 37 CFR 1.14. This collection is estimated to take 6 minutes to complete, including gathering, preparing, and submitting the completed application form to the USPTO. Time will vary depending upon the individual case. Any comments on the amount of time you require to complete this form and/or suggestions for reducing this burden, should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, U.S. Department of Commerce, P.O. Box 1450, Alexandria, VA 22313-1450. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.